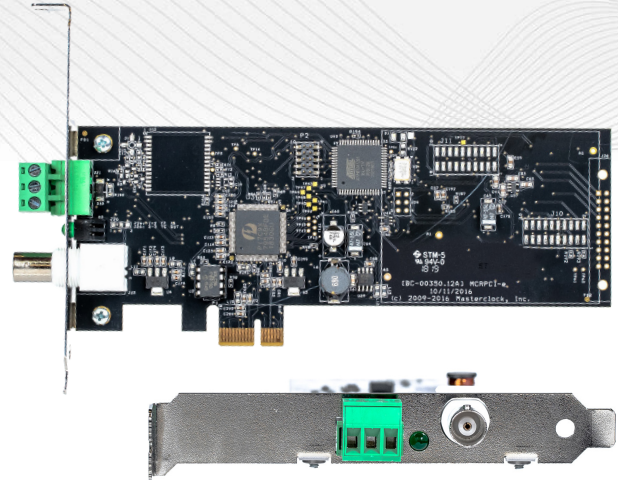


## Standard Features

- Synchronizes PC to  $\pm 2$  milliseconds of time code reference
- References IRIG-B, SMPTE, EBU time codes
- Automatic time code detection
- Balanced and unbalanced time code inputs
- Provides millisecond accuracy to Windows applications
- Fully configurable time zone and Daylight Saving Time offsets
- PCIe revision 2.0 – slot compatible X1 through X16
- LED status indicator



## Specifications

### TIME CODE INPUTS

- SMPTE (30/25/25 fps - non-drop frame only) to Leitch Date Encoding Standard
- IRIG-B pulse width coded (unmodulated) DC, IEEE 1344 standard
- IRIG-B(1) 1 kHz Amplitude Modulated, IEEE 1344 standard

### SUPPORTED DRIVERS

- Windows XP (32 bit)
- Windows 7/8/10 (32 & 64 bit)
- Windows Server 2003 R2/2008 R2/2010 R2/2012 R2/2016
- Linux

### SOFTWARE

- API software development kit and documentation is provided for designers
- Windows OS requires Net Framework 4.0 and a Windows Visual Studios C++ to be installed

### PHYSICAL

- Length: 6.6 in (167.6 mm), Height: 2.712 in (68.8 mm)
- Low profile card and bracket standard; Full height bracket included

### OPERATING PARAMETERS

- Temperature: 0 to 60 °C
  - MTBF: 1,570,000
- All calculated using Fixed/Ground Mil HDBK 217F assumption

### COMPLIANCE

- FCC, ROHS, CE Marked, ANSI

### HOLDOVER ACCURACY

- With loss of power or time reference synchronization, device reverts to an internal battery-backed real-time clock chip with accuracy of  $\pm 1$ min/year

### HIGH-STABILITY OSCILLATOR OPTIONS

- HSO-1 with TCXO provides accuracy of  $\pm 3$  seconds/year
- HSO-2 with OXCO provides typical stability of  $\pm 250$ ms/year after 30 days of aging

Freq = 10 Mhz	HSO-1	HSO-2
Oscillator Type	TCXO	OCXO
Freq. Stability	$\pm 2.5 \times 10^{-6}$	$\pm 5 \times 10^{-9}$
Aging Stability per yr	$\pm 1 \times 10^{-6}$	$\pm 1 \times 10^{-8}$
Drift per year	$\pm 3$ seconds	$\pm 0.3$ seconds

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